

BlueX Microelectronics Co., Ltd.

Bluetooth 5.0 LE | MESH SoC Evaluation Kit Guide

BX2400-dRF0xp-S1x

Version 1.7

May 19th, 2021 released



http://www.bluexmicro.com





Contents

1.	Prefa	ace	 3
2.	Prep	aration	
	2.1	SDK Software Preparation	 3
	2.2	Kit Preparation	 5
	2.3	Install Keil and Jlink	 5
	2.4	Firmware Programming Example	 5
3.	Proc	edure	
	3.1	Copy document to specific file	 7
	3.2	Install J-Flash, and program firmware to evaluation kit	 7
	3.3	Confirm output data from evaluation kit, and check the advertising	 11
4.	Revi	sion History	 14
5.	Арре	endix	
	5.1	Schematic of BX2400-dRF0xp-S1c	 15



1. Preface

In this guide, we'll introduce the steps of how to program firmware of advertising Bluetooth into the evaluation kit:

- (1) Copy document to specific file.
- (2) Install J-Flash, and program firmware [template_with_bootloader.hex] to evaluation kit.
- (3) Confirm output data from evaluation kit, and check the advertising.

2. Preparation

- 2.1 SDK Software Preparation
 - (1) Taking BlueX SDK3.2 for example, download link as below: https://gitee.com/BXMicro/SDK3

BXMicro / SDK3						© Wato
< \∕ \ 代码	🖅 Issues 🕕	🕄 Pull Requests 🕕	🖽 Wiki	屾 统计	🗢 DevOps	s *
master 🔹 💲 分支 1 😒 标签	ž3	+ Pull	Request + Issue	1 文件 ▼ Web IDE	克隆/下载 ▼	简介
B bx-lz 1.Fix that bx_public	cannot be publis	hed t f791ecc 3个月前		HTTPS S	SH SVN SVN+SSH	
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C components	1.Fb	that bx_public cannot be pub	lished to multiple subscr	ibers	3个月前	贡献者 (2)
🗋 devices	first	commit			8个月前	1 B

(2) The software Demo Code is also available for developers' reference. Download link as below: https://gitee.com/BXMicro/SDK3_Demo

BXMicro / SDK3_E	Demo					◎ Watch •
<⇒℃代码	🗊 Issues 🕕	Pull Requests 0	🖽 Wiki	Ш 统计	∞ DevO	ps 🕶
该仓库未指定开源许可证,非	未经作者的许可,此	(代码仅用于学习, 不能用于复	他用途。 😮	1 🔨	×	简介 新无描述
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B bx-lz SDK3.2 821a1a4	3个月前			HTTPS	SSH SVN SVN+SSH	
🗋 demo	SDK	3.2		https://	/gitee.com/BXMicro/SDK3	_Dem 复制
README.MD	rena	me readme file			▲ 下载ZIP	
image-2020081814004010	06.png add	led demo		2	8个月前	



(3) When unzipping Demo Code, please move the Demo to Examples File of SDK3 subdirectory.



(4) We also provide software introduction for developers, page link as below: <u>https://gitee.com/BXMicro/SDK3_DOC</u>

BXMicro / SDK	3_DOC				
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该仓库未指定开源许可证	E, 未经作者的许可, 」	比代码仅用于学习,不能用于算	其他用途。 😮		×
master 🔹 🕅 分支 1 🛇	标签 0	+ Pul	I Request + Issue	文件 🕶 Web IDE	克隆/下载 ▼
B bx-lz add button 7	7c3bb36 3个月前				34次提交
亡 adc使用	Vers	sion SDK3.2			3个月前
ble_master	add	pdf files			7个月前
ble_slave	del	unused key_value			7个月前
🛅 button	add	button			3个月前
🗋 debug_log	Vers	sion SDK3.2			3个月前
合 flexible_button库使用	Vers	sion SDK3.2			3个月前
🛅 iic_test	Vers	sion SDK3.2			3个月前
🖺 key_value	Vers	sion SDK3.2			3个月前
亡 pwm使用	add	pdf files			7个月前
🗀 sensor	add	example doc			8个月前
🛅 spi_test	Vers	sion SDK3.2			3个月前
🛅 timer	Vers	sion SDK3.2			3个月前
☐ 快速入门	Vers	sion SDK3.2			3个月前



2.2 Kit Preparation

Get the evaluation kit of BX2400-dRF0xp-S1c, RF0x Module, and adapted board. Picture below takes RF03 Module for example.



2.3 Install Keil and Jlink

For procedure, please check the "System Installation of Evaluation Kit."

2.4 Firmware Programming Example

(1) Click [ble_base] under the path of SDK3.2.

*	B	3XMicro-SDK3-release-v3.2	-20210104 > SDK3	> examples	> ble > ble_base > project > mdk
	名称	修改日期	类型	大小	
	ble_base.uvoptx	2021/1/4 16:35	UVOPTX 文件	66 KB	
*	🔢 ble_base.uvprojx	2021/1/4 16:35	礣ision5 Project	108 KB	
*					
A.					



(2) In Keil, revise the value of [BX_DEEP_SLEEP] under [bx_sdk3_config.h] to be O(zero), then programing the firmware. This will enable/disable the sleep mode while chips running. The purpose of it is for easier to connect Jlink and read datalog since it may be hard to connect when enabling sleep mode. Developers decide whether enable or disbale according to customized demand.

🔌 🎬 🕮 🥔 🏭 🔓 template	🔍 孫二書 🍝 🧇 🏙
Project 🛛 📮 🔀	bx_sdk3_config.h*
Project: ble_base Image: template Image: bx/log Image: bx/log	bd_subs_toingut 14 15 16 17 /* Define to prevent recursive inclusion*/ 18 #ifndefBX_SDK3_CONFIG_H
bx_config.h bx_sys_config.h bx_pcb_config.h bx_app_config.h bx_ip_config.h bx_ip_config.h	29 //8 => rf08 30 =#ifndef BX_CHIP_TYPE 31 #define BX_CHIP_TYPE 32 #endif 33 34 - 35 =#ifndef BX_DEEP_SLEEP 36 #define BX_DEEP_SLEEP 37 #endif 38 - 39 =#if (BX_DEEP_SLEEP > 0) 40 #define BX_UART_SHELL_ENABLE 0 41 #endif

(3) After programming finished, firmware [template_with_bootloader.hex] will be in the same path as below.

	» BXMicro-SDK3-release-v	3.2-20210104 SDK3	> example
名称	修改日期	类型	大小
Listings	2021/1/19 11:47	文件夹	
Objects	2021/1/19 11:48	文件夹	
ble_base.uvoptx	2021/1/4 16:35	UVOPTX 文件	66 KE
🔢 ble_base.uvprojx	2021/1/4 16:35	礦ision5 Project	108 KE
	2021/1/19 11:48	Intel HEX binary	12 KE
鬙 debug_flash.ini	2021/1/19 11:48	MS ini file	1 KB
鬙 template.asm	2021/1/19 11:48	Assembly langu	1,613 KB
📄 template_ota.bin	2021/1/19 11:48	BIN 文件	49 KB
template_with_bootloader.	hex 2021/1/19 11:48	Intel HEX binary	129 KB



3. Procedure

- 3.1 Copy document to specific file
 - In the path of SDK3\tools\bluex\prog_tool_v2, both [BlueX] file and [JFlashDevice.xml] should be copied to Jlink subdirectory as below.

	1. J. 1997 March 1997		SDK3 > tools > blu	ıex → prog_tool_v2 →
	名称	修改日期	类型	大小
*	BlueX JLinkDevices.xml	2021/1/9 12:05 2021/1/4 16:35	文件夹 XML 文档	1 KB
* *	📄 ReadMe.txt	2021/1/4 16:35	文本文档	1 KB
*				

> SEGGER > JL	nk V644f >		
^	-		
名称	修改日期	类型	大小
📜 BlueX	2020/4/25 15:55	文件夹	
Devices	2020/4/25 15:23	文件夹	
📕 Doc	2020/4/25 15:23	文件夹	
📕 ETC	2020/4/25 15:23	文件夹	
GDBServer	2020/4/25 15:23	文件夹	
📜 RDDI	2020/4/25 15:23	文件夹	
📕 Samples	2020/4/25 15:23	文件夹	
USBDriver	2020/4/25 15:23	文件夹	
🔜 JFlash.exe	2019/4/12 23:18	应用程序	704 KB
🔝 JFlashLite.exe	2019/4/12 23:18	应用程序	345 KB
🚼 JFlashSPI.exe	2019/4/12 23:18	应用程序	408 KB
🛃 JFlashSPI_CL.exe	2019/4/12 23:18	应用程序	563 KB
🔜 JLink.exe	2019/4/12 23:18	应用程序	292 KB
JLink_x64.dll	2019/4/12 23:19	应用程序扩展	17,268 KB
JLinkARM.dll	2019/4/12 23:18	应用程序扩展	16,184 KB
🔜 JLinkConfig.exe	2019/4/12 23:18	应用程序	441 KB
🗋 JLinkDevices.xml	2019/7/22 18:58	XML 文档	1 KB
🛃 JLinkDLLUpdater.exe	2019/4/12 23:18	应用程序	139 KB
🚟 JLinkGDBServer.exe	2019/4/12 23:18	应用程序	599 KB
🚟 JLinkGDBServerCL.exe	2019/4/12 23:18	应用程序	575 KB

3.2 Install J-Flash, and program firmware to evaluation kit.



(1) Click Click choose [create a new project], and then [start J-Flash].



(2) Click the button indicated by red arrow as below to choose Target Device.

eate New Project		
Target Device		
Cortex-M0		
Little endian 💌		1
T	0	-
l arget Interrace	Speed (KHZ)	
SWD 🗾	4000	-
		ОК
		QIX

(3) Roll down to choose BlueX.

anufacturer	×				
Manufacture	Abov		Core	Flash size	RAM size
Inspecified	active-semi		4BM7		
Inspecified	Altera		ABM9	0	2
Inspecified	AmbigMicro AMS Analog Atmel AyDeeKay		ABM11		
Inspecified			Cortex-45		
Inspecified			Cortex-A7	<u>.</u>	
Inspecified			Cortex-A8		2
Inspecified	BlueX	_	Cortex-A9	×	~
Inspecified	Cirrus Logic		Cortex:A12		
Inspecified	Cypress		Cortex:A15		<u>.</u>
Inspecified	Dialog Semiconductor		Cortex-A17	Q.	2
Inspecified	Digi		Cortex-A53	~	~
Inspecified	DSPGroup		Cortex-A57		
Inspecified	Epson		Cortex-M0		
Inspecified	Faraday		Cortex-M0		
Inspecified	GigaDevice		Cortex-M1	~	
Inspecified	Hilscher		Cortex-M3		
Inspecified	IDT	~	Cortex-M4	6	2
Inspecified	UDI U	ortex-M7	Cortex-M7	(a)	
Inspecified	C	ortex-M23	Cortex-M23		*
Inspecified	C	ortex-M33	Cortex-M33		
Inspecified	C	ortex-R4	Cortex-R4	<u>e</u> .	8
Inspecified	C	ortex-R5	Cortex-R5	2	2
Inspecified	C	ortex-R8	Cortex-R8		*
Inspecified	B	X	BX		
Inspecified	B	ISC-V	RISC-V	<u>0</u>	÷ .
hov	Δ	C33M6128L	Cortex-M3	128 KB	12 KB

(4) Choose program according to Module Flash voltage, then click OK. Choose [Apollo_00_3V3] when using BX2416/RF03/RF04 Module, or [Apollo_00_1V8] when using RF08.

lanufacturer	Device	Core	Flash size	RAM size
ueX	Apollo_00_1V8	Cortex-M0	8192 KB	192 KB
ueX	Apollo_00_3V3	Cortex-M0	8192 KB	192 KB



(5) Target Device Installed. Now setting the Speed by choosing 4000kHz, and click OK.

Create New Project	×
Target Device	
BlueX Apollo_00_3V3	
Little endian 💌	
- Target Interface	Speed (kHz)
SWD -	4000 -
	<u>0</u> K

(6) Connect the evaluation kit with Jlink as below.

两开关下拨	连接到J-LINK(SWD)

(7) Operate J-Flash to link to evaluation kit. In J-Flash, click [Target] and then [Connect]. If it fails, please check #(10).

🔜 SEGGER J-Flash V6.44f - [new project *]	-	×
File Edit View Target Options Window Help		
Project - nev Connect Name Disconnect Host connection Test		
Taget interface Inf SWD speed Manual Programming F7 SWD speed Manual Programming →		
MCU BlueX.Apollo_00_3V3 Core CortexM0 Endian Little Check core ID No Use target RAM 132 KB @ 0x100000		
Flash menory Determal bank 0 Base address Flash size 8152 KB		
Project closed Creating new project Twe project created successfully Commercing vis USB to J-Link device 0 - Out of syme, resynchronizing - Out of syme, resynchronizing - Out of syme, resynchronizing - Ust of syme, resynchronizing - EXBOR: Famot connect to J-Link vis USB. - EXBOR: Failed to connect. Could not establish a connection to the J-Link. 		۲. «
Ready		//



(8) When success, pull [template_with_bootloader.hex.hex] into J-Flash software.

File Edit View	ash V6.44f - [new project Target Options W	t*] indow Help																	-		×
Project - nev	v p	E:\BULE>	(\2_式 0√800	で構造	料∖软	件相关	€\SDK	\BXM	icro-	SDK	3-rel	ease	-v3.2	2-202	1010	04\SI	DK3\	example	e 🗖		—
Host connection	USB [Device 0]	Address	0	1	2	3	4 5	6	7	8	9	A	в	С	D	E	F	ASCII			_
Target interface	SWD	800000	42	58	32	34	00 80	1 12	ดด	10	14	ØØ	RA	19	82	12	66	BX24			
Init SWD speed	4000 kHz	000010	00	00	04	00	00 00		00	20	20	20	60	25			20	2.1.0 1.1			
SWD speed	4000 kHz	800010	99	00	04	00	00 00	9 107	00	68	00	38	60	35	11	61	29			; 5	,
		800020	B0	B5	69	46	42 78	3 00	AF	92	06	92	ØF	D3	1D	DB	08	iFB	×		
MCU	BlueX Apollo_00_3V3	800030	DB	00	CB	18 '	9D 46	5 05	00	69	46	00	78	01	FØ	8E	F9		FiF	·.×	
Core	Cortex-M0	800040	07	21	Ø1	20	6B 78	3 60	46	1 B	Ø7	1 B	ØF	DA	10	ØВ	40	. * . k	×1F	1	2
Endian	Little	000010	00	40	~~	E0 -	DD 44		40	40	410	00	44	00	n 0	na	nn		R 00		-
Check core ID	No	000020	70	40	HZ	56.	60 H	5 10	40	43	IE	70	41	69	B 2	рө	עם	.e. \.	r.ec.		
Use target RAM	192 KB @ 0x100000	800060	F 8	B5	6A	46	44 78	3 00	AF	84	06	84	ØF	E3	1D	DB	08	jFD	×		
		800070	DB	00	D3	18 '	9D 46	5 05	00	22	00	00	21	68	46	D9	F6		F".	. thF.	
Flash memory	Internal bank 0	800080	4F	FD	87	22	6B 78	8 6F	46	1 B	Ø7	1 B	ØF	D9	10	13	40	N 9	NRF		2
Base address	0x800000	000000	a.	20		40	00 70		Г 4	20	40	20	00	<u>.</u>	TO.	20	10				-
Flash size	8192 KB	600070	90	эн	28	40 .	20 70	3 72	24	67	40	44	99	OT.	гө	67	r7		XFI 1	····1	
		8000A0	BD	46	F8	BD :	10 B!	5 06	4C	06	4B	9C	42	00	D3	10	BD	.F	L.¥	(.B	
		8000B0	62	68	20	68	00 2:	L D9	F6	32	FD	08	34	F4	E7	CØ	46	bh h.	t2.	.41	P
		800000	04	94	12	ดด	АС 9 4	1 12	ดด	70	B 5	Ø5	ดด	07	48	ØF	ØØ		n	н	
		000000	14	00	00	DO .	 	2 124	20	22	88	20	99	40	49	00	TO			\ @D	
		000000	17		00	re .	r3 r0		20	34		27			-14	66	re			/.eb.	
		8000E0	11	FA	90	18	43 43	2 58	41	сы	82	20	BD	85	10	C1	64	C	BX8	p	
		8000F0	80	21	FØ	B5 🗆	3F 41	B 40	4C	1B	69	85	BØ	22	00	18	00	?	KCL.i	i"	
		800100	49	00	00	93 1	01 F(3 84	F8	3C	4B	3D	4D	22	68	2B	60	I	<	(=M"h+`	
		800110	30	48	90	42	4C D	1 30	48	30	49	28	6.0	00	98	62	6.9	CH BL	(1474	н` Ы	
		000110	30	10	10	10		1 10	-10	00	-10		DO	00	20	202	40	1			-
LOG																					23
- J-Flash V6.4 - JLinkARM dll	started 4f (J-Flash compiled Apr V6.44f (DLL compiled Ap piect	r 12 2019 17:1 pr 12 2019 17:	7:25) 17:02)																	^
- New project Close project	created successfully																				
Creating new pr	ojeat																				
Opening data fil - Data file op	created successfully le [E:\BULEX\2_文档资料) ened successfully (44086	软件相关\SDK\ 5 bytes, 2 ran	BXMid ges,	ro-SI CRC (K3-r f da	eleas ta =	e=v3.2 0xCA5F	-2021 7D15,	0104\ CRC	SDK3 of f	\exam ile =	ples 0x3	\ble' FF4D:	\ble_ 303)	base	\pro	ject'	,mdk\ble	_base_v	vi th_boo	tlo
<																					>
			_		_				_		_				_	-	-		_		
Ready																					

(9) Click [Target] and then [Production Programming], or click F7. It will start to download as pictures below. If it fails, please check #(10).

SECORP LE	sch V6 44f - Inew projec	+ *1														_	. r	7	×
Je Sedder J-Ha	isii voleen - [new projec																	_	~
File Edit View	Target Options W	/indow Help	_																
Project - nev	Connect			软件样	∃¥\\$		XMic	ro-SI	DK3-re	elease	-v3.2	-202	1010	04\SI)K3\	example			x
Name	Disconnect						.1												_
Host connection			00		×1	<u>×2</u> :	×4												
Host connection	lest	>	1 2	3	4	5	6	7	8 9	A	в	С	D	E	F	ASCLL			
Target interface	Production Proc	ramming E7	8 3	2 34	99	80	12 1	00 1	C 14	4 00	60	19	82	12	90	BX24			
Init SWD speed	M I D		10 0	1 00	88	00	02 1	00 0	18 01	A 38	60	35	11	Ø 1	29			5)	
SWD speed	Manual Program	nming >	5 4	1 46	42	79	88	00 0	2 84	6 92	AP	n2	10	np	00	iPDv	,	J/	
MCU	BlueX Apolo 10, 3/3	999928 DB	89 C	2 10	40	46	AC 1	AA 4	0 14	6 99	70	01	TO	OT	EQ		48.0		
Core	Cortex-M0	800030 DB	00 0	D 1H	70	40	05 1	00 0	7 40		~~	DT D	10	OL OL	17		. 11 . X	••••	
Endian	Little	800040 07	21 0	20	РВ	28	6C ·	46 1	B 0	2 18	ØF	DH	10	ЮВ	40		F	· · · · e	
Check core ID	No	800050 98	40 A:	2 5C	BD	46	10 .	40 4	13 11	E 98	41	СØ	B2	BØ	BD	.e.\.F.	eca	• • • •	
Use target RAM	192 KB @ 0x100000	800060 F8	B5 61	46	44	78	00 I	AF (14 00	5 A4	ØF	E3	1D	DB	08	jFD×.			
Els de manuel	Internal Issuel: O	800070 DB	00 D:	3 1A	9D	46	Ø5 I	00 3	2 00	00 6	21	68	46	D9	F6	F.	."!	hF	
Race address		800080 4E	FD Ø	7 22	6 B	78	6E •	46 1	B Ø	7 1B	ØF	D9	10	13	40	N. Viskn	F	e	
Flash size	8192 KB	800090 06	3A 91	1 40	28	78	72 !	54 (9 40	5 22	00	01	FØ	69	F9	.:.0(xr	TiF".	i.	
		8000A0 BD	46 F	3 BD	10	B5	Ø6 ·	4C (16 41	B 9C	42	00	DЗ	10	BD	.F	L.K.B		
		8000B0 62	68 21	3 68	00	21	D9 1	F6 3	2 FI	D Ø8	34	F4	E7	CØ	46	bh h.t.	.24	F	
		800000 04	94 1	2 00	RC	94	12 1	00 1	20 B	5 05	ØØ	02	48	ØF	00		n	н	
		999909 14		, DO	50	120	R 1 -	20 1	2 8	a 20	88	40	42	88	FØ		2.54	ap	
		999950 11	DO 0	3 10	42	42	E0 .	A1 1	10 0	0 27	50	10	10	C1	84	CDV	۰./.۱	c	
		000010 00	04 D	9 1D	-13	40	40	40		5 /0	no	00	10	10	00	+ 2VO	np.		
		0000100 00	21 F	5 CG E	31	40	40	46 1	D D:	7 65	00	22	66	10	66		L.1		
		800100 49	00 0	9 43	01	FØ	84	F8 .	IC 41	8 30	40	22	68	5.8	60	1	- <r=m< th=""><th>"h+</th><th></th></r=m<>	"h+	
		800110 3C	4B 9	42	4C	D1	3C 4	4B 3	BC 48	8 2B	60	00	9B	62	68	<k.bl.<< th=""><th>К<Н+.</th><th>bh</th><th>-</th></k.bl.<<>	К<Н+.	bh	-
		000100 01	20 11	> 10	00	64	na i	. 99	10.00		ha	00	20	20	AD	L.		100	
LOG																			8
Application log	started																		~
- J-Flash V6.44	If (J-Flash compiled Ap WE AAF (DIL compiled A	r 12 2019 17:17:25)																	
Creating new pro	ject	pr 16 6010 11.11.067																	
- New project of	reated successfully																		
- Project close	•d																		
Creating new pro	ject																		
- New project of	reated successfully [\於住相关\SDE\BYWG or	-SDK3			3 2-2	02101	04351	F31.07		ALL.	hl.	hare	Inno	ant's	ndk\bla ba	ra with	heat	10
- Data file ope	ened successfully (4408	6 bytes, 2 ranges, 0	IRC of	data "	0xC	ASF7D	15, C	RC of	file	= 0x3	FF4D	303)	pase	122.0	NOL 1	mon for e_be		_0000	
																			~
<																			<u>ار</u>
Ready																			



Project - ne	wp	The support																	
Name	Value	Address	0x60	0000		5	d 12	×4											
Host connection	USB [Device 0]		10					Ē		0		n	0				40.011		-
Tarrat interface	swb	Hadress	10	1	2	3 .	1 5	6	7 8		H	B	6	9	E	P	HSCII		-
Init SWD speed	4000 kHz	800000	92	58	5Z	39.1	90 80	12	00 10	14	66	66	19	82	12	66	BK24		
SWD speed	4000 kHz	888818		99	84	99.6	30 00	07 1	69 68	88	3B	68	35	11	81	29		; '5)	
		888828	B0	B5	69	46 4	12 78	00 I	AF 93	86	92	ØF	D3	1D	DB	68	iFBx		
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Core	Cortex-M0	888848	82	21	81	20 6	B 78	6C ·	46 11	87	1B	ØF	DA	10	ØВ	48	.t. kxlF		
Endian	Little	899959	98	40	02	5C 1	RD 46	10	48 4	11	98	41	CB	82	BB	RD	R . F. RC		
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Use target HAM	132 KB @ 0x100000	800060	10	85	БН	10 1	11 78	00	HP H	1 86		or	23	10	DB	60			
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Flash size	8192 KB	0	Targe 4.790	t eras	sed, pr	rogram	nmed an	nd verif	fied suc	cessfu	lly - C	omp	leted	after	×		N"kxnF .:.@(xrTiF' .FL.K. bh h.t.2	"i. .B .4F	
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(10) If connection or programming fail, pull up P16 and try restarting the power or doing reset, and back to #(7) as below.



- 3.3 Confirm output data from evaluation kit, and check the advertising
 - (1) Confirm P16 is pulled down after download completed. Try restarting the power or doing reset.





(2) Click **V6.44**f and then [rtt_viewer]. It's the access of configuration.



(3) In configuration, click the button indicated by red arrow as below to manage Specify Target Device.

🔜 J-Link RTT Viewer V6.44f Configuration	?	\times
Connection to J-Link		
O <u>T</u> CP/IP		
O <u>E</u> xisting Session		
Specify Target Device		
Apollo_00_3V3	~	
Script file (optional)	1	
Target Interface & Speed		
SWD	4000 kł	{z ▼
RTT Control Block		
● Auto Detection ○ Address / ○ Sea	rch <u>R</u> ang	;e
OK	Car	ncel

(4) Choose [BlueX], and choose program according to Flash voltage, then click OK. When using BX2416/RF03/RF04 Module, please choose [Apollo_00_3V3].

🔝 Target Device S	ettings				×
Selected Device: AC	C33M6128L			Little En	dian 🔻 Core #0 💌
Manufacturer	Device	Core	NumCores	Flash Size	RAM Size
BlueX	*	*	*	*	*
BlueX	Apollo 00 1V8	Cortex-M0	1	8 MB	192 KB
BlueX	Apollo_00_3V3	Cortex-M0	1	8 MB	192 KB
				OI	Cancel

(5) In configuration, set up [Target Interface& Speed] as below.

🔜 J-Link RTT Viewer V6.44f Config	juration	?	\times
- Connection to J-Link			
• USB _ Serial No			
○ <u>T</u> CP/IP			
O Existing Session			
Specify Target Device			
Apollo_00_3V3		\sim	
Script file (optional)			
Target Interface & Speed			
SWD	-	4000 kH	z 🔻
RTT Control Block			
• Auto Detection O Address	🔘 Seau	ch <u>R</u> ange	2
	OK	Can	cel



(6) When setting correct, the output will be as below.



(7) Finally, confirm the advertising is on. Check mobile phone with App (NRF connect) linked to BLUEX-SDK3.

 ■ Devices
 STOP SCANNING
 :

 SCANNER
 BONDED
 ADVERTISER

 SDK
 ~ ×

 ③
 BLUEX-SDK3
 CONNECT

 66:55:44:33:22:11
 NOT BONDED
 -34 dBm

 NOT BONDED
 -34 dBm
 ↔ 1292 ms



4. Revision History

Version	Description	Date (YYYY/MM/DD)	Author
1.0	Initial version	2020/03/01	姚琪
1.1	Content amended and pictures added	2020/03/02	陈仕玮
1.2	Partially amend the content of EVK	2020/07/17	简任锋
1.3	Add EVK pictures and J-Flash programming instruction	2020/08/01	简任锋
1.4	Amend pictures, and add content of firmware programming	2021/01/19	简任锋
1.5	Add introduction of SDK download and procedure	2021/04/13	简任锋
1.6	Add Schematic of BX2400-dRF0xp-S1c	2021/04/25	陈玥瑶
1.7	Update English version	2021/05/19	Michelle

5. Appendix

5.1 Schematic of BX2400-dRF0xp-S1c

